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1 Metaphor and metonymy in object-oriented design patterns   ◼
James Noble, Robert Biddle, Ewan Tempero
January 2002 **Australian Computer Science Communications , Proceedings of the twenty-fifth Australasian conference on Computer science - Volume 4 ACSC '02**, Volume 24 Issue 1
Publisher: Australian Computer Society, Inc., IEEE Computer Society Press

Full text available: ⅋ pdf(909.06 KB)   Additional Information: full citation, abstract, references, citings, index terms

The key principle of object-oriented design is that each program object should correspond to an object in the real world, that is to say, a program is a *metaphor* for the world. More advanced object-oriented designs, such as many of Gamma et. al.'s *Design Patterns,* are not directly metaphorical: State objects, Strategy objects and Visitor objects, for example, do not correspond to objects in the real world. We show how these patterns, and other similar designs, can be understood as ...

Keywords: design, metaphor, metonymy, object-orientation

2 Session 6A: Architectures: An evolutive architecture reasons as a semiotic,   ◼
hermeneutic and autopoietic entity
Albertina Lourenci
September 2001 **Proceedings of the 4th International Workshop on Principles of Software Evolution**
Publisher: ACM Press
Full text available: ⅋ pdf(447.59 KB)   Additional Information: full citation, abstract, references, index terms

To build evolutive architectural and implementation code levels isomorphically to a domain level that generates sustainable cities autopoietically is challenging due to: 1) the likening of software to art is still in its infancy [14], 2) the highly interactive nature of the cooperative game exhibited by the experts has just begun to emerge [2] and 3) the needs of higher order reasoning structures mimicking the most evolved human cognitive processes such as reflective abilities, semiotic, hermene ...

Keywords: architecture, autopoietic, domain, hermeneutic, objects, reflection, semiotic

3   ◼
Technical papers: component technologies: Pluggable reflection: decoupling meta-

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1 Analysis methodology: Issues on simulation and optimization I: simulation-based   ◼
retrospective optimization of stochastic systems: a family of algorithms
Jihong Jin, Bruce Schmeiser
December 2003 **Proceedings of the 35th conference on Winter simulation: driving innovation**
Publisher: Winter Simulation Conference
Full text available: 📄 pdf(141.57 KB)   Additional Information: full citation, abstract, references

> We consider optimizing a stochastic system, given only a simulation model that is
> parameterized by continuous decision variables. The model is assumed to produce
> unbiased point estimates of the system performance measure(s), which must be expected
> values. The performance measures may appear in the objective function and/or in the
> constraints. We develop a family of retrospective-optimization (RO) algorithms based on a
> sequence of sample-path approximations to the original problem with increas ...

2 Session 14: middleware support for multimedia: A pluggable service-to-service   ☐
◈ communication mechanism for home multimedia networks
Jin Nakazawa, Hideyuki Tokuda
December 2002 **Proceedings of the tenth ACM international conference on Multimedia**
Publisher: ACM Press
Full text available: 📄 pdf(436.61 KB)   Additional Information: full citation, abstract, references

> This paper proposes a pluggable service-to-service (S2S) communication mechanism in a
> middleware for home networks, called Virtual Networked Appliance (VNA) architecture. In
> the architecture, service description method and the plug-gable S2S communication
> mechanism are separated in an orthogonal way. Through the separation, VNA architecture
> solved problems of home networks on which users have to operate multiple
> heterogeneous middleware technologies simultaneously: middleware fragmentation
> probl ...

3 SFCGen: A framework for efficient generation of multi-dimensional space-filling   ☐
◈ curves by recursion
Guohua Jin, John Mellor-Crummey
March 2005 **ACM Transactions on Mathematical Software (TOMS)**, Volume 31 Issue 1
Publisher: ACM Press
Full text available: 📄 pdf(402.27 KB)   Additional Information: full citation, abstract, references, index terms

> Because they are continuous and self-similar, space-filling curves have been widely used

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Kukharenko, B.G.;
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Digital Object Identifier 10.1109/PHYCON.2003.1236907

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☐ 2. **Orthogonal wavelet analysis of Lorenz system**
Kaiyun Zhu; Shuxun Wang;
Signal Processing, 2004. Proceedings. ICSP '04. 2004 7th International Confei
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Digital Object Identifier 10.1109/ICOSP.2004.1452630

AbstractPlus | Full Text: PDF(143 KB) **IEEE CNF**
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Cuomo, K.M.; Oppenheim, A.V.; Strogatz, S.H.;
Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactic
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Volume 40, Issue 10, Oct. 1993 Page(s):626 - 633
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communication systems
Chi-Chung Chen; Kung Yao;
Circuits and Systems I: Fundamental Theory and Applications, IEEE Transacti
Circuits and Systems I: Regular Papers, IEEE Transactions on]
Volume 47, Issue 12, Dec. 2000 Page(s):1663 - 1672
Digital Object Identifier 10.1109/81.899918

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☐ 5. **The optimized process and property test for Lorenz chaotic sequences**
Wang Ying; Zheng DeLing; Ju Lei;
Communications, Circuits and Systems, 2005. Proceedings. 2005 Internationa

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Jianfeng Wu; Shibin Jiang; Tao Luo; Jihong Geng; Peyghambarian, N.; Barnes
Photonics Technology Letters, IEEE
Volume 18, Issue 2, Jan. 15, 2006 Page(s):334 - 336
Digital Object Identifier 10.1109/LPT.2005.861970

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□   2. **Joint management of finished goods inventory and demand process for ɛ product: a computational approach**
Liuxin Chen; Youyi Feng; Jihong Ou;
Automatic Control, IEEE Transactions on
Volume 51, Issue 2, Feb. 2006 Page(s):258 - 273
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□   3. **Dynamic voltage scaling of mixed task sets in priority-driven systems**
Dongkun Shin; Jihong Kim;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction:
Volume 25, Issue 3, March 2006 Page(s):438 - 453
Digital Object Identifier 10.1109/TCAD.2005.853706

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□   4. **JBEAM: multiscale curve coding via beamlets**
Xiaoming Huo; Jihong Chen;
Image Processing, IEEE Transactions on
Volume 14, Issue 11, Nov. 2005 Page(s):1665 - 1677
Digital Object Identifier 10.1109/TIP.2005.857273

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□   5. **Intra-task voltage scheduling on DVS-enabled hard real-time systems**
Dongkun Shin; Jihong Kim;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction:
Volume 24, Issue 10, Oct. 2005 Page(s):1530 - 1549
Digital Object Identifier 10.1109/TCAD.2005.852036

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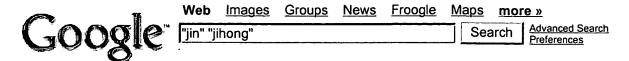## [PDF] To: University of Wisconsin-Stout Faculty, Staff and Students From ...
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Authors   Jihong Jin   1929 Danube Way, Bolingbrook, IL
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Conf.Pages 332-336. Author Biographies **Jihong Jin** Received A Ph.d From The School Of Industrial
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